

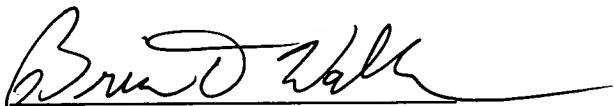
claims have been canceled, the Examiner's rejections are now moot. The Applicant will direct their comments to the relevance of Paneth, et al. to the newly presented claims.

The Examiner cites Paneth, et al. as illustrating circuitry for disabling a receiver port of the UART. The reference pointed to by the Examiner at column 34, line 28-31, refers to the ability of commands to enable and disable a channel. The newly presented claims recite disabling and enabling the receiver in response to an indication of full-duplex mode of operation (Claim 1), or disabling and enabling a receiver in response to an indication of at least one of whether the apparatus operates in full-duplex mode, the receiver enable flag is set and the means for storing contains data for transmission (Claims 18 and 21). The Paneth, et al. reference does not disclose controlling enabling and disabling a receiver based upon these parameters. Thus, a Notice of Allowance for the newly submitted claims is respectfully requested.

The Applicant respectfully submits that Claims 11-21 are allowable over the art of record and respectfully requests a Notice of Allowance.

Respectfully submitted,

By:



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Date: July 18, 1997

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